

## **Amendments to the Specification**

Please delete the graph on page 9 in its entirety.

Please replace the Brief Description of Drawings as follows:

### **Brief Description of Drawings**

Fig. 1A is a side view of the sense resistor incorporated into a powered circuit.

Fig. 1B is a perspective view of the sense resistor incorporated into a powered circuit.

Fig. 2A is a schematic circuit diagram of the sense resistor with a Positive Temperature Coefficient (PTC) thermistor configuration.

Fig. 2B is a graph of a thermistor response.

Fig. 3 is a schematic circuit diagram of the sense resistor with a Negative Temperature Coefffficient (NTC) themistor configuration.

Fig. 4 is a schematic circuit diagram of the sense resistor with a Positive Temperature Coefficient (PTC) thermistor configuration & Negative Temperature Coefficient (NTC) themistor configuration.